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GROUP 2100

PATENT
TESSERA 3.3-018

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of
Khandros et al

Application No.: 08/030,194

Filed: April 28, 1993

For: SEMICONDUCTOR CHIP ASSEMBLIES,
METHODS OF MAKING SAME AND
COMPONENTS FOR SAME

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: Group Art Unit: 2104
:
: Examiner: D. Russele
: Date: July 18, 1994

Hon. Commissioner of Patents
and Trademarks
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

It is respectfully requested that the references listed on the enclosed Form PTO-1449 be made of record and considered with respect to the above-referenced U.S. Patent application. A copy of each reference is enclosed. Submission of the present Information Disclosure Statement should not be taken as an admission that the cited references are legally available prior art or that the same are pertinent or material.

Respectfully submitted,

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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class mail in an envelope addressed to Commissioner of Patents and Trademarks, Washington, D.C. 20231 on July 18, 1994

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